**Cadence Rapid Adoption Kit: (Lab & Lecture material, database included as attachment (.zip folder))**

Article (20416388) Title: Allegro Front-to-Back Design Flow Tutorial (Complete PCB flow using ConceptHDL schematic: From netlist import to Artwork generation)

**URL:**  [https://support.cadence.com/apex/ArticleAttachmentPortal?id=a1Od000000050NrEAI](%20https%3A//support.cadence.com/apex/ArticleAttachmentPortal?id=a1Od000000050NrEAI)

Article (20482026) Title: Allegro PCB Editor Flow – Initial PCB Design Creation

URL:  [https://support.cadence.com/apex/ArticleAttachmentPortal?id=a1O0V00000911htUAA](https://urldefense.proofpoint.com/v2/url?u=https-3A__apac01.safelinks.protection.outlook.com_-3Furl-3Dhttps-253A-252F-252Fsupport.cadence.com-252Fapex-252FArticleAttachmentPortal-253Fid-253Da1O0V00000911htUAA-26data-3D02-257C01-257Cchandubt-2540hcl.com-257C2356a3533d6541d7a84708d690bb3e1d-257C189de737c93a4f5a8b686f4ca9941912-257C0-257C0-257C636855531233720919-26sdata-3DmqmvMOsboYX4a1Sh8bIn7bORdosxGj6SZxc7B8sNxPU-253D-26reserved-3D0&d=DwMFAg&c=aUq983L2pue2FqKFoP6PGHMJQyoJ7kl3s3GZ-_haXqY&r=9RRI7RTEp3f7fYP5R7SN2Luk9_-ktGw8FtcQVQ8LELk&m=e7xCQDGRxqEyYgxpgCLKqi7cB5aC3dQiJ_ARNkVFla8&s=OhjhHJxtEAeSkeipo2waa33IM7GDKrRGM8Tap5_er1g&e=)

 Article (20482120) Title: How to use Schematic Driven Placement and By user pick features in Quickplace

URL:  [https://support.cadence.com/apex/ArticleAttachmentPortal?id=a1O0V00000912FHUAY](https://urldefense.proofpoint.com/v2/url?u=https-3A__apac01.safelinks.protection.outlook.com_-3Furl-3Dhttps-253A-252F-252Fsupport.cadence.com-252Fapex-252FArticleAttachmentPortal-253Fid-253Da1O0V00000912FHUAY-26data-3D02-257C01-257Cchandubt-2540hcl.com-257C2356a3533d6541d7a84708d690bb3e1d-257C189de737c93a4f5a8b686f4ca9941912-257C0-257C0-257C636855531233720919-26sdata-3D9SLZetCWg0ZVjDfykSAazgaVkSXeiUQtOAEmU0qpTH4-253D-26reserved-3D0&d=DwMFAg&c=aUq983L2pue2FqKFoP6PGHMJQyoJ7kl3s3GZ-_haXqY&r=9RRI7RTEp3f7fYP5R7SN2Luk9_-ktGw8FtcQVQ8LELk&m=e7xCQDGRxqEyYgxpgCLKqi7cB5aC3dQiJ_ARNkVFla8&s=r71cWLa74Q9Yhbf6xlDl3gWCCGxkL4LL-G-si0IwWDs&e=)

 Article (20482028) Title: Allegro PCB Editor Flow – Electrical Constraints

URL:  [https://support.cadence.com/apex/ArticleAttachmentPortal?id=a1O0V00000911iYUAQ](%20https%3A//support.cadence.com/apex/ArticleAttachmentPortal?id=a1O0V00000911iYUAQ)

 Article (20482029) Title: Allegro PCB Editor Flow – Physical and Spacing Constraints

URL:  [https://support.cadence.com/apex/ArticleAttachmentPortal?id=a1O0V00000911idUAA](%20https%3A//support.cadence.com/apex/ArticleAttachmentPortal?id=a1O0V00000911idUAA)

 Article (20482030) Title: Allegro PCB Editor Flow – Routing and High Density Interconnect

URL:  [https://support.cadence.com/apex/ArticleAttachmentPortal?id=a1O0V00000911inUAA](%20https%3A//support.cadence.com/apex/ArticleAttachmentPortal?id=a1O0V00000911inUAA)

 Article (20482031) Title: Allegro Front to Back Flow – Backdrill, Post Processing, and Report Generation

URL:  [https://support.cadence.com/apex/ArticleAttachmentPortal?id=a1O0V00000911ixUAA](%20https%3A//support.cadence.com/apex/ArticleAttachmentPortal?id=a1O0V00000911ixUAA)

 Article (20483685) Title: How to Use 3D Canvas in Allegro PCB Editor

URL:  [https://support.cadence.com/apex/ArticleAttachmentPortal?id=a1O0V000009EVwIUAW](%20https%3A//support.cadence.com/apex/ArticleAttachmentPortal?id=a1O0V000009EVwIUAW)

**Analysis:**

Article (20482430) Title: Analysis Workflows – Impedance Analysis (RAK)

URL: <https://support.cadence.com/apex/ArticleAttachmentPortal?id=a1O0V000009ESKkUAO>

Article (20482267) Title: Analysis Workflows: Coupling Simulation

URL: <https://support.cadence.com/apex/ArticleAttachmentPortal?id=a1O0V000009ES2AUAW>

Article (20417572) Title: Sigrity: Static IR Drop Analysis (Video)

URL: <https://support.cadence.com/apex/ArticleAttachmentPortal?id=a1Od000000050h5EAA>

 Article (20448036) Title: IC Component Setup in the Allegro Sigrity PI Power Feasibility Editor (Video)

URL: <https://support.cadence.com/apex/ArticleAttachmentPortal?id=a1Od0000005xFoxEAE>

 Article (20448041) Title: Analyzing a Decap Configuration in the Allegro Sigrity PI Power Feasibility Editor (Video)

URL: <https://support.cadence.com/apex/ArticleAttachmentPortal?id=a1Od0000005xFpMEAU>

 **More information:**

 Schematic Symbol creation using Allegro PCB Librarian XL

 Article (20483964) Title: Allegro PCB Librarian (Video)

URL:  [https://support.cadence.com/apex/ArticleAttachmentPortal?id=a1O0V000009EWPGUA4](%20https%3A//support.cadence.com/apex/ArticleAttachmentPortal?id=a1O0V000009EWPGUA4)

 Article (20417988) Title: Creating Copper Planes in Allegro PCB Editor (Video)

URL:  [https://support.cadence.com/apex/ArticleAttachmentPortal?id=a1Od000000050nnEAA](%20https%3A//support.cadence.com/apex/ArticleAttachmentPortal?id=a1Od000000050nnEAA)

 Article (20419146) Title: High-Speed Constraint Management using Allegro PCB Editor (Video)

URL:  [https://support.cadence.com/apex/ArticleAttachmentPortal?id=a1Od0000000516TEAQ](%20https%3A//support.cadence.com/apex/ArticleAttachmentPortal?id=a1Od0000000516TEAQ)

Article (20417242) Title: Creating and Applying a Replicated Circuit within the Allegro PCB Editor (Video)

URL:  [https://support.cadence.com/apex/ArticleAttachmentPortal?id=a1Od000000050blEAA](%20https%3A//support.cadence.com/apex/ArticleAttachmentPortal?id=a1Od000000050blEAA)